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JAN 09 2006

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

Complete If Known

Application Number	10/675,778
Filing Date	09/30/2003
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Chan, Eddie P.
Art Unit	2183
Attorney Docket No.	AUS920030480US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
DC AA	4,034,353	07-1977	Denny et al.	
	AB 4,821,178	04-1989	Levin et al.	
	AC 5,151,981	09-1992	Westcott et al.	
	AD 5,581,482	12-1996	Wiedenman et al.	
	AE 5,594,864	01-1997	Trauben	
	AF 5,691,920	11-1997	Levine et al.	
	AG 5,758,168	05-1998	Mealey et al.	
	AH 6,480,966	11-2002	Rawson, III	
	AI 2003/0135720	07-2003	DeWitt et al.	
	AJ 5,926,640	07-1999	Mason et al.	
	AK 6,185,652	02-2001	Shek et al.	
	AL 4,145,735	03-1979	Soga	
	AM 5,548,762	08-1996	Creedon et al.	
	AN 4,794,472	12-1988	Doyama	
	AO 6,161,187	12-2000	Mason et al.	
	AP 3,707,725	12-1972	Dellheim	
	AQ 6,408,386	06-2002	Hammond et al.	
	AR 6,775,728	08-2004	Zimmer et al.	
	AS 6,757,771	06-2004	Christie	
	AT 6,148,321	11-2000	Hammond	
↓	AU 6,275,893	08-2001	Bonola	
↓	AV 4,825,359	04-1989	Ohkami et al.	

FOREIGN PATENT DOCUMENTS

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NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
DC AW	Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 nd Ed., 1982, pp.434-443.
DC AX	Inoue, "Digital mobile communication system designed for nationwide police activities - WIDE system", IEEEExplore, October 1996, pp. 1-2.

Examiner: /Dillon Cody/ (05/18/2006)

Date Considered: 05/18/2006

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PATE
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ENCLOSURE

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Examiner Name	Cody, Dillon J.
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Attorney Docket No.	AUS920030480US

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DC	AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

Examiner: /Dillon Cody/ (05/18/2006)

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DC AA	5,710,881	01-20-1998	Gupta et al.	
	AB 5,822,763	10-13-1998	Baylor et al.	
	AC 5,928,334	07-27-1999	Mandyam et al.	
	AD 6,094,709	07-25-2000	Baylor et al.	
	AE 6,285,974 B1	09-04-2001	Mandyam et al.	
	AF 6,636,950 B1	10-21-2003	Mithal et al.	
	AG 2004/0205302 A1	10-14-2004	Cantrill	
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DC AN	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.		
AO	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.		
AP	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.		
AQ	Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages		
AR	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2		
AS	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html		

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